

2. (Amended) A liquid crystal display device having a plurality of thin film transistors, at least one of said thin film transistors comprising:

C<sup>1</sup>  
an active layer having source and drain regions over an insulating surface;

a gate insulating film over said active layer;

a gate electrode over said gate insulating film; and

two wirings connected to said source and drain regions,

wherein edge portions of said active layer and a part of an edge portion of said two wirings are aligned, and

wherein the gate insulating film is in contact with the two wirings and not in contact with the edge portions of the active layer.

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21. (Amended) A liquid crystal display device having a plurality of thin film transistors, at least one of said thin film transistors comprising:

an active layer over an insulating surface;

a gate insulating film over said active layer;

a gate electrode over said gate insulating film; and

two wirings connected to said active layer,

C<sup>2</sup>  
wherein a part of an edge portion of at least one of two wirings is aligned with at least one edge portion of the active layer, and

wherein the gate insulating film is in contact with the two wirings and not in contact with the at least one edge portion of the active layer.

22. (Amended) A semiconductor device comprising:

at least one thin film transistor comprising:

an active layer over an insulating surface;

a gate insulating film over the active layer; and

a gate electrode over the gate insulating film,

a first insulating film over the thin film transistor;

first and second wirings connected to the active region through contact holes in the first insulating film,

a second insulating film over the first insulating film;

wherein a part of an edge portion of at least one of first and second wirings is aligned with at least one edge portion of the active layer,

wherein the gate insulating film is in contact with the first and second wirings and not in contact with the at least one edge portion of the active layer, and

wherein the second insulating film is in contact with the insulating surface.

23. (Amended) A semiconductor device comprising:

at least one thin film transistor comprising:

an active layer over an insulating surface;

a gate insulating film over the active layer; and

a gate electrode over the gate insulating film,

a first insulating film over the thin film transistor;

C2 first and second wirings connected to the active region through contact holes in the first insulating film,

a second insulating film over the first insulating film;

wherein a part of an edge portion of one of the first and second wirings is aligned with an edge of the active layer,

wherein the gate insulating film is in contact with the first and second wirings and not in contact with the edge of the active layer, and

wherein the second insulating film is in contact with the insulating surface.

24. (Amended) A semiconductor device comprising:

at least one thin film transistor comprising:

an active layer over an insulating surface;

a gate insulating film over the active layer; and

a gate electrode over the gate insulating film,

a first insulating film over the thin film transistor;

first and second wirings connected to the active region through contact holes in the first insulating film,

a second insulating film over the first insulating film;

c2 wherein a part of an edge portion of the first wiring is aligned with one of edge portions of the active layer, and a part of an edge portion of the second wiring is aligned with another one of the edge portions of the active layer,

wherein the gate insulating film is in contact with the first and second wirings and not in contact with the edge portions of the active layer, and

wherein the second insulating film is in contact with the insulating surface.

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